

# SPECIFICATION

COMMERCIALY AVAILABLE

ITEM: DIELECTRIC CERAMIC FILTER

PART NUMBER: CF-3000025

No Pure Tin

6/20/2019: added Process Map for NPT. Revise process temperature.

| ISSUED                  | CHECKED | CHECKED | CHECKED       | APPROVED       |
|-------------------------|---------|---------|---------------|----------------|
| 11/2007 XX              |         |         | 08/27/2013 GL | 08/27/2013 TFG |
| 6/20/19 <sup>(PG)</sup> |         |         |               |                |
|                         |         |         |               |                |

**FILTRONETICS Inc**

1. APPLICATION

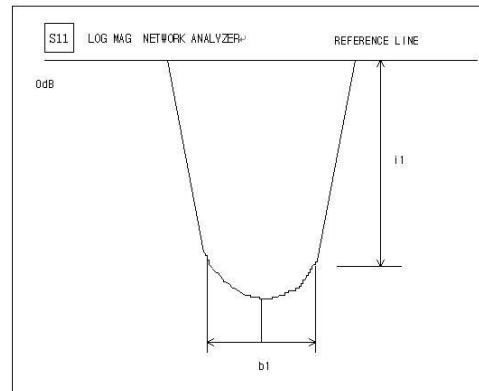
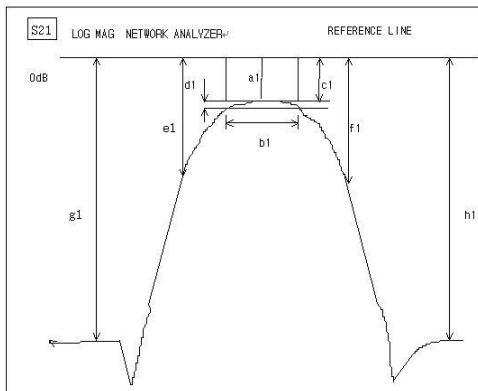
THIS SPECIFICATION APPLIES TO A BAND PASS FILTER USING DIELECTRIC RESONATORS.

2. PART NUMBER

|           |              |
|-----------|--------------|
| PART NO   | CF-30000025  |
| PACKAGING | PLASTIC TRAY |

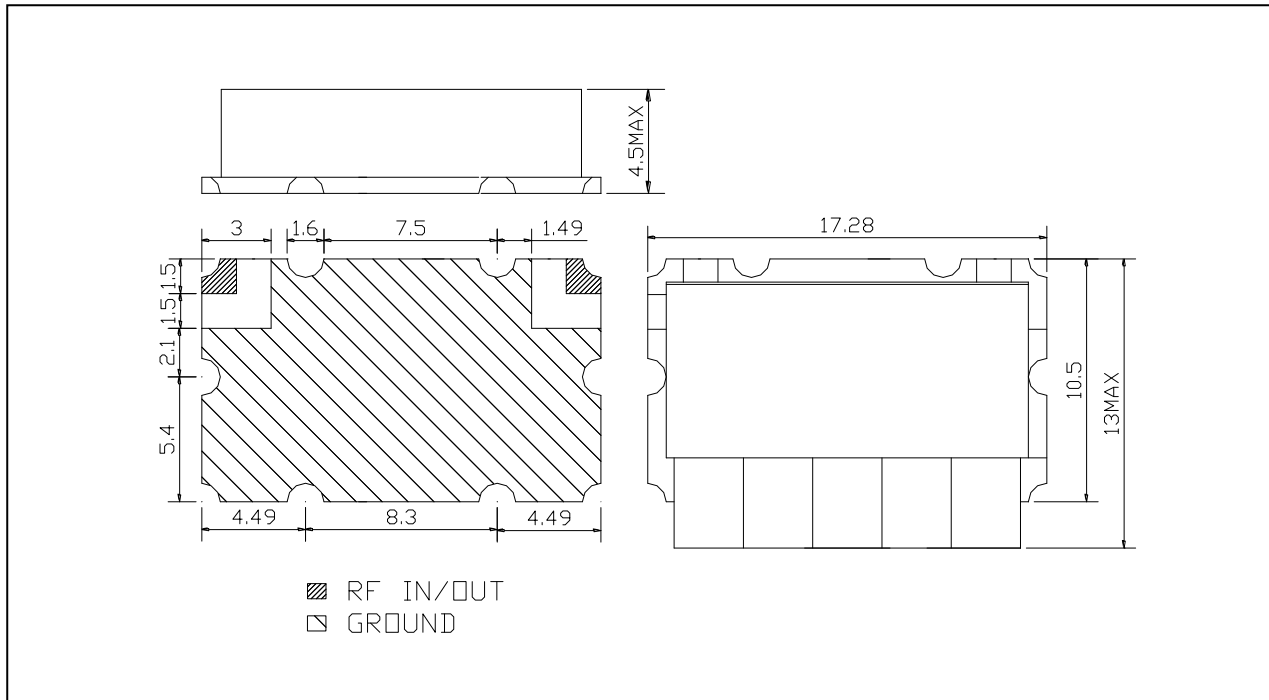
3. SPECIFICATIONS

| NO | ITEMS                           | Ref.        | SPECIFICATION    |
|----|---------------------------------|-------------|------------------|
| 1  | Center Frequency (Fo)           | a1          | 3000 MHz         |
| 2  | 3.0dB Band Width (=PB)          | b1          | 2 MHz Min        |
| 3  | Insertion Loss AT Fo            | a1          | 5.5 dB Max       |
| 4  | V.S.W.R IN Fo+/-1MHz            | -           | 1.75:1 Min       |
| 5  | Attenuation<br>[absolute value] | At 2920 MHz | 40 dB Min        |
|    |                                 | At 3080 MHz | 50 dB Min        |
| 6  | Impedance                       | -           | 50Ω              |
| 7  | Maximum Input Power             | -           | 1 W (+30dBm)     |
| 8  | Operating Temperature Range     | -           | -35 ~ +85℃       |
| 9  | Workmanship                     | -           | IPC-610 class 3  |
| 10 | Process Temperature             | -           | +150℃ for 1 hour |





5. DIMENSIONS



- MATERIAL SPECIFICATION**
1. PCB
    - 1) MATERIAL: FR4
    - 2) TERMINALS: Sn/Pb, HASL,
  2. METAL CASE
    - 1) Ag plated brass
  3. RESONATOR
    - 1) COATING MATERIAL: silver plate, 8~30um
  4. INTERNAL SOLDER
    - 1) Sn96.5/Ag3.5 Lead Free solder, 221 deg C melting
  5. RESONATORS TABS
    - 1) Sn/Pb(9:1) Plated brass (Can not be pure Tin)  
Or Ag Plates
  6. NO PURE TIN ALLOWED

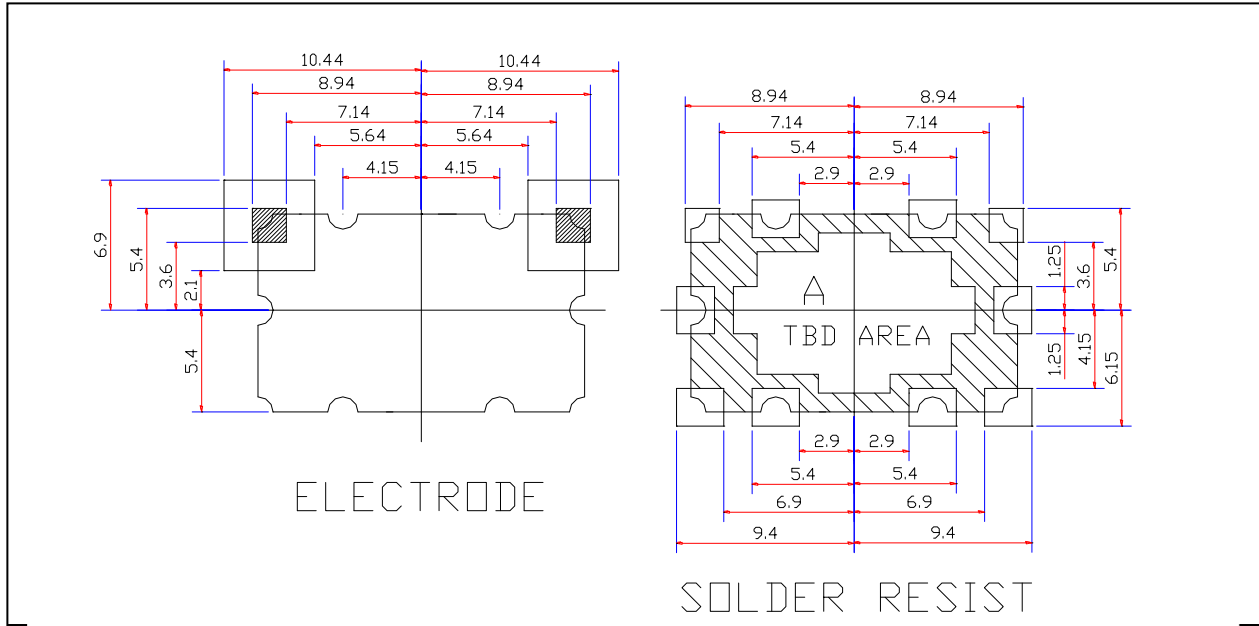
**UNIT: MM**  
**TOLERANCE: +/-0.5MM**  
**IN/OUT LAND: +/-0.3MM**

**MARKING**  
 Label Material: High temp polyimide  
 Marking:  
 CF-30000025  
 Filtronetics, Inc  
 Date Code

● **CAUTIONS:**

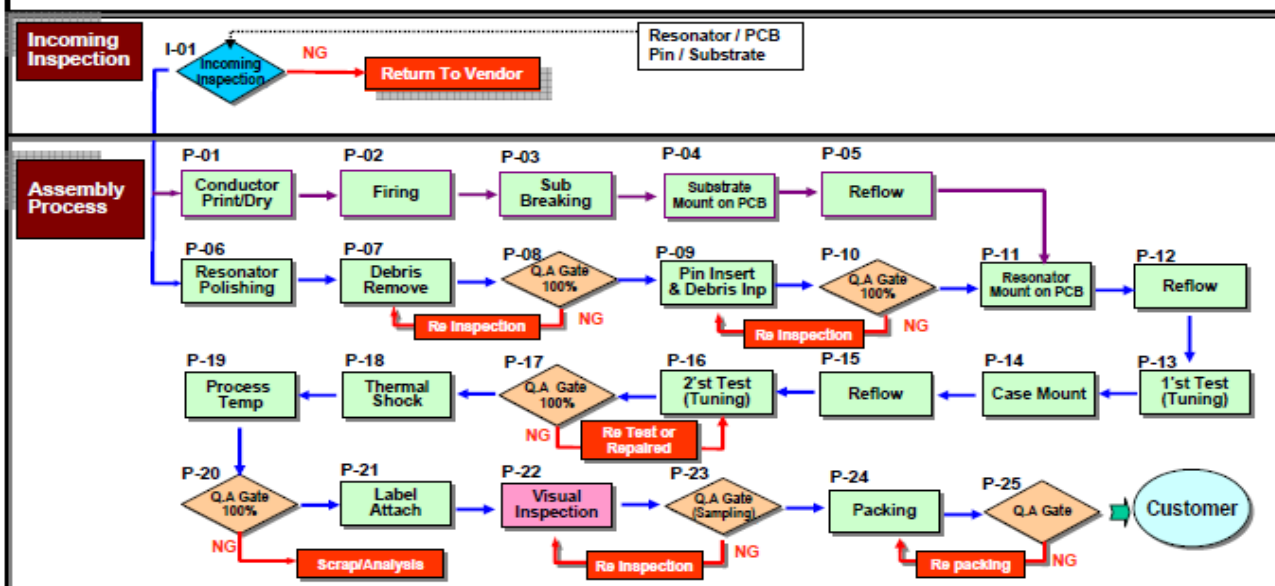
1. When handling products, be careful not to damage the outer-electrode.
2. When handling products be careful not to touch the outer-electrode with bare hands or solderability is reduced.
3. Do not apply excessive pressure or shock to product in handling or in transportation or damage to the ceramic filters may result.

Recommended Solder Pattern:



| Quality Conformance Test 100% Final inspection 100% Test Data |  |
|---|--|
| 1)  | Visual mechanical to IPC-610 Class 3 Including inspection for cleanliness. |
| 2)  | Thermal Shock -44 to +85°C 10 cycles                                       |
| 3)  | Electrical Specifications at ambient                                       |

Process Map for Filtronetics NPT

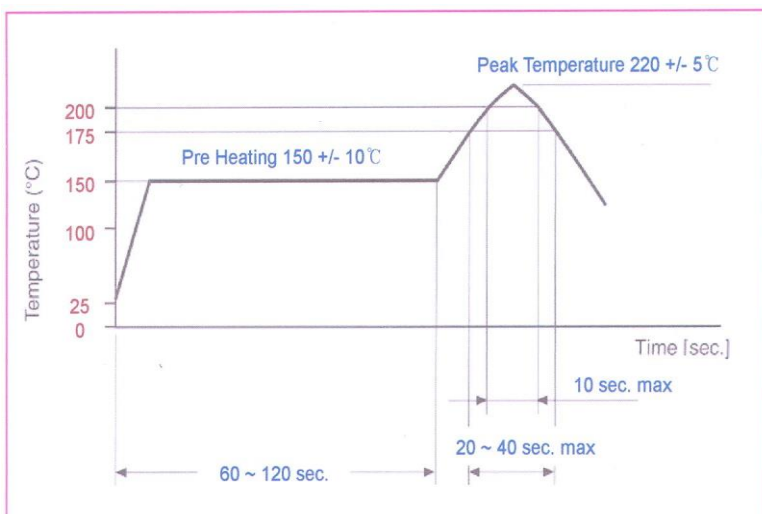


- Note : 1. At I- 01: IQA must inspect PCB to make sure of good Castellation with no loose edges.
- 2. At P-08 and P-10: No debris left in the hole of resonator.
- 3. At P-17:100% Electrical test and visual inspection of solder joint If there is a loose debris of solder due to tuning process, Must be returned to P-16 to repair
- 4. At P-18: Thermal Shock Condition - -44 to +85°C / 10 Cycles.
- 5. At P-19: Process Temp Condition - +150 °C / 1HR + 230 °C / 10 MIN.
- 6. At P-17 and P-20: Q.A MUST record the test result.
- 7. At P-23: Sampling Plan-Visual (GII/AQL=0.65)
- 8. At P-24:Packing method - Tray and Vinyl bag

6. DEFINITIONS

| TERMS                | DESCRIPTION   | SPECIFICATION    |
|----------------------|---|------------------|
| Center Frequency     | The midpoint of through band pass filter pass band, normally expressed as the arithmetic mean of the -3db point. Also called fo.  | 3. SPECIFICATION |
| Pass Band Width      | The width of the pass band of a filter referenced to the minimum insertion loss point in the pass band. The pass band of a filter is stated as -1.0dB bandwidth.  |                  |
| Insertion Loss       | The loss of the filter, in db, measured at center frequency relative to a through line (0 dB).  |                  |
| Attenuation          | Reduction of RF power through a filter measured in dB, at desired band and referenced to 0 dB. (Filter to be removed from circuit)  |                  |
| Pass Band Ripple     | Variations in loss in the pass band of the filter, superimposed upon the fundamental shape of the pass band.  |                  |
| V.S.W.R in Pass Band | The ratio of the maximum value of a standing wave to its minimum value, related to the return loss in pass band.  |                  |
| Date Code            | Each date code shall be from a single lot   |                  |
| Lot                  | One Batch of parts processed in a single manufacturing run. A single lot shall have no more than one firing, plating, soldering, or other batch processing.   |                  |
| Cleanliness          | Parts shall be clean. They shall be free from smudges, loose solder, solder spatter, metal chips or mold release agents. No burrs. Particles or any foreign material over 0.2 mm which might detract from the intended operation, function or appearance of the part. |                  |

7. REFLOW SOLDERING STANDARD CONDITIONS FOR TIN/LEAD



- Measuring point of temperature in-out terminals of the device.
- Reflow Soldering
- Both convection and infrared rays
- Hot air
- Hot plates
- Solder Cream: Sn64/Pb36

8. RELIABILITY TEST AND CONDITIONS

| ITEM                                    | TEST CONDITIONS   | REQUIREMENTS  |
|---|---|---|
| Resistance to solder heat               | Preheat temperature : 120 to 150 °C<br>Preheat time: 1 to 1.5 min<br>Solder temperature: 260 +/- 10 °C<br>Dipping time: 10 +/- 0.5 sec<br>→Soldering Time : 5sec.max.per each terminal  | No damage such as cracks should be caused in chip element.  |
| Solderability                           | Preheat temperature: 120 to 150 °C<br>Preheat time: 1 to 1.5 min<br>Solder temperature: 235 +/- 5 °C<br>Dipping time: 5 +/- 1 sec   | More than 80% of the terminal electrode shall be covered with new solder  |
| Heat resistance (High-temperature Load) | Temperature: 85 +/- 2 °C<br>Applied voltage: Rated voltage<br>Applied current: Rated current<br>Recovery: 1 to 2hrs of recovery under the standard condition after the removal from test chamber.   | No mechanical damage. After test, the device shall satisfy the specification in section 3.<br>Standard condition is 25+/-5 °C and Less than 65% relative humidity |
| Thermal shock (Temperature cycle)       | Conditions for 1 cycle<br>Step 1: + 85 °C 15 min<br>Step 2 : - 44 °C 15 min<br>Number of cycle: 10  | No mechanical damage. After test, the device shall satisfy the specification in section 3.  |
| Humidity Resistance                     | Temperature: 40 +/- 2 °C<br>Humidity: 90 to 95% RH<br>Duration: 96 +/- 5 hrs<br>Recovery: 1 to 2hrs of recovery under the standard condition after the removal from test chamber.   | No mechanical damage. After test, the device shall satisfy the specification in section 3.  |
| Vibration                               | The device is subjected to vibration of 2 sweeps in each of three mutually perpendicular planes. Frequency shall be varied within 10~50Hz with 1.5mm double amplitude and within 50~500 Hz with 10g acceleration. Sweep time of Frequency shall be 15minutes. | No mechanical damage. After test, the device shall satisfy the specification in section 3.  |
| Shock                                   | The device is subjected to 3 shocks in each direction of six mutually perpendicular planes. Each shock shall be a half-sine wave shaped with a magnitude of 30g and a duration of 11msec.   | No mechanical damage. After test, the device shall satisfy the specification in section 3.  |